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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/501,431 **Confirmation No.:** 9438
Applicants: S. A. Garyainov, et al.
Filing Date: July 13, 2004
Priority Date: March 1, 2002
Title: Fluxless Assembly of Chip Size Semiconductor Packages
Group Art Unit: 2812
Examiner: Walter L. Lindsay, Jr.
Docket No.: 02PCTEZ1

Los Angeles, California
November 22, 2005

Via Facsimile to (571) 273-8300

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION DATED 08/23/2005

Dear Sir:

In reply to the Office action of 08/23/05, please amend the above-identified Application as follows.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.